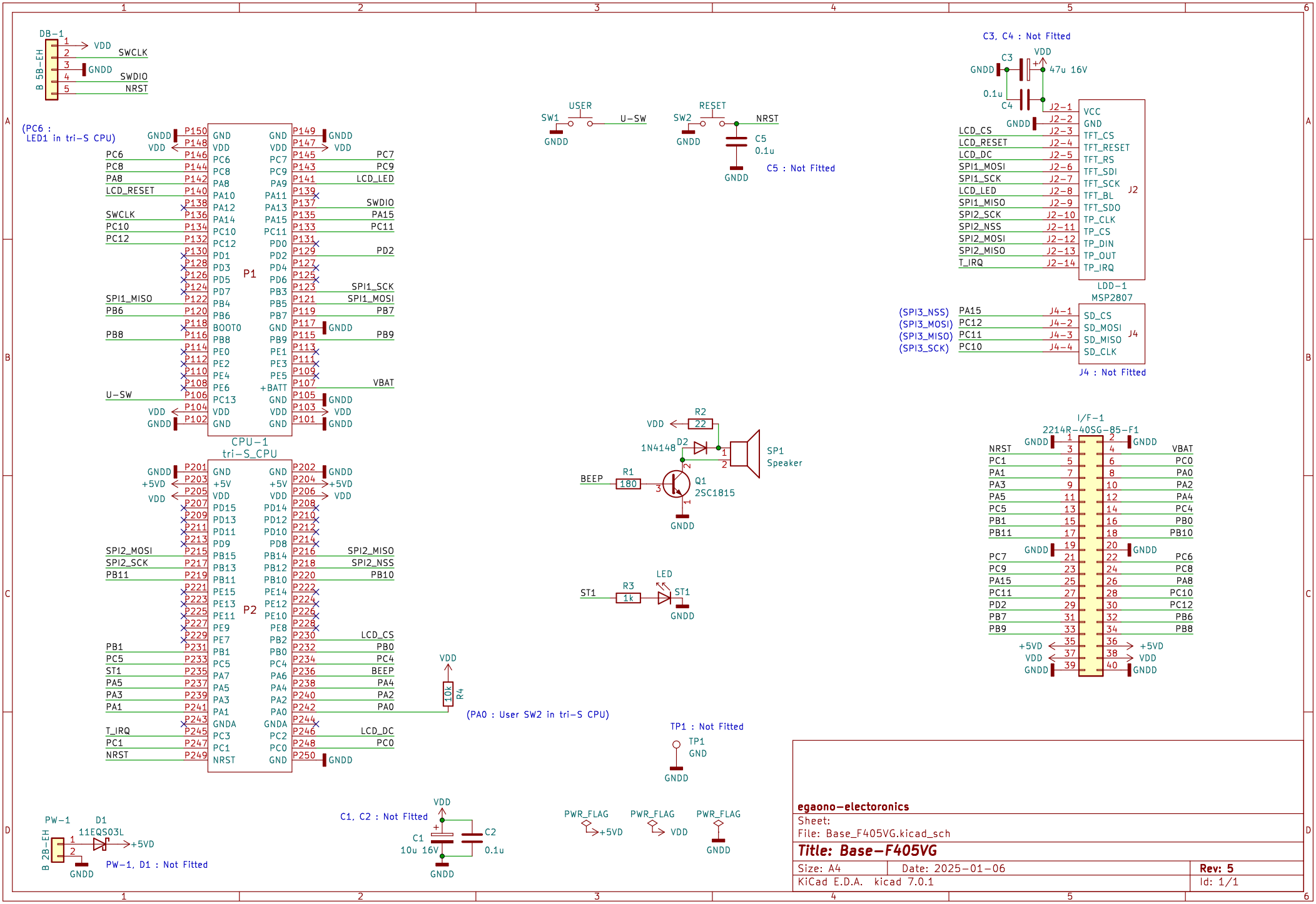
DB-1
B 5B-EH
VDD
SWCLK
GNDD
SWDIO
NRST
(PC6 :
LED1 in tri-S CPU)
P1
CPU-1
tri-S_CPU
P2
USER
SW1
U-SW
RESET
SW2
NRST
C5
0.1u
C5 : Not Fitted
C3, C4 : Not Fitted
C3
47u 16V
0.1u
C4
J2
LDD-1
MSP2807
LCD_CS
LCD_RESET
LCD_DC
SPI1_MOSI
SPI1_SCK
LCD_LED
SPI1_MISO
SPI2_SCK
SPI2_NSS
SPI2_MOSI
SPI2_MISO
T_IRQ
TFT_CS
TFT_RESET
TFT_RS
TFT_SDI
TFT_SCK
TFT_BL
TFT_SDO
TP_CLK
TP_CS
TP_DIN
TP_OUT
TP_IRQ
(SPI3_NSS)
(SPI3_MOSI)
(SPI3_MISO)
(SPI3_SCK)
J4
SD_CS
SD_MOSI
SD_MISO
SD_CLK
J4 : Not Fitted
R2
22
1N4148
D2
SP1
Speaker
BEEP
R1
180
Q1
2SC1815
ST1
R3
1k
LED
I/F-1
2214R-40SG-85-F1
R4
10k
(PA0 : User SW2 in tri-S CPU)
TP1 : Not Fitted
TP1
PW-1
D1
11EQS03L
B 2B-EH
PW-1, D1 : Not Fitted
C1, C2 : Not Fitted
C1
10u 16V
C2
0.1u
PWR_FLAG
+5VD
VDD
GNDD
egaono-electoronics
Sheet:
File: Base_F405VG.kicad_sch
Title: Base-F405VG
Size: A4
Date: 2025-01-06
Rev: 5
KiCad E.D.A. kicad 7.0.1
Id: 1/1