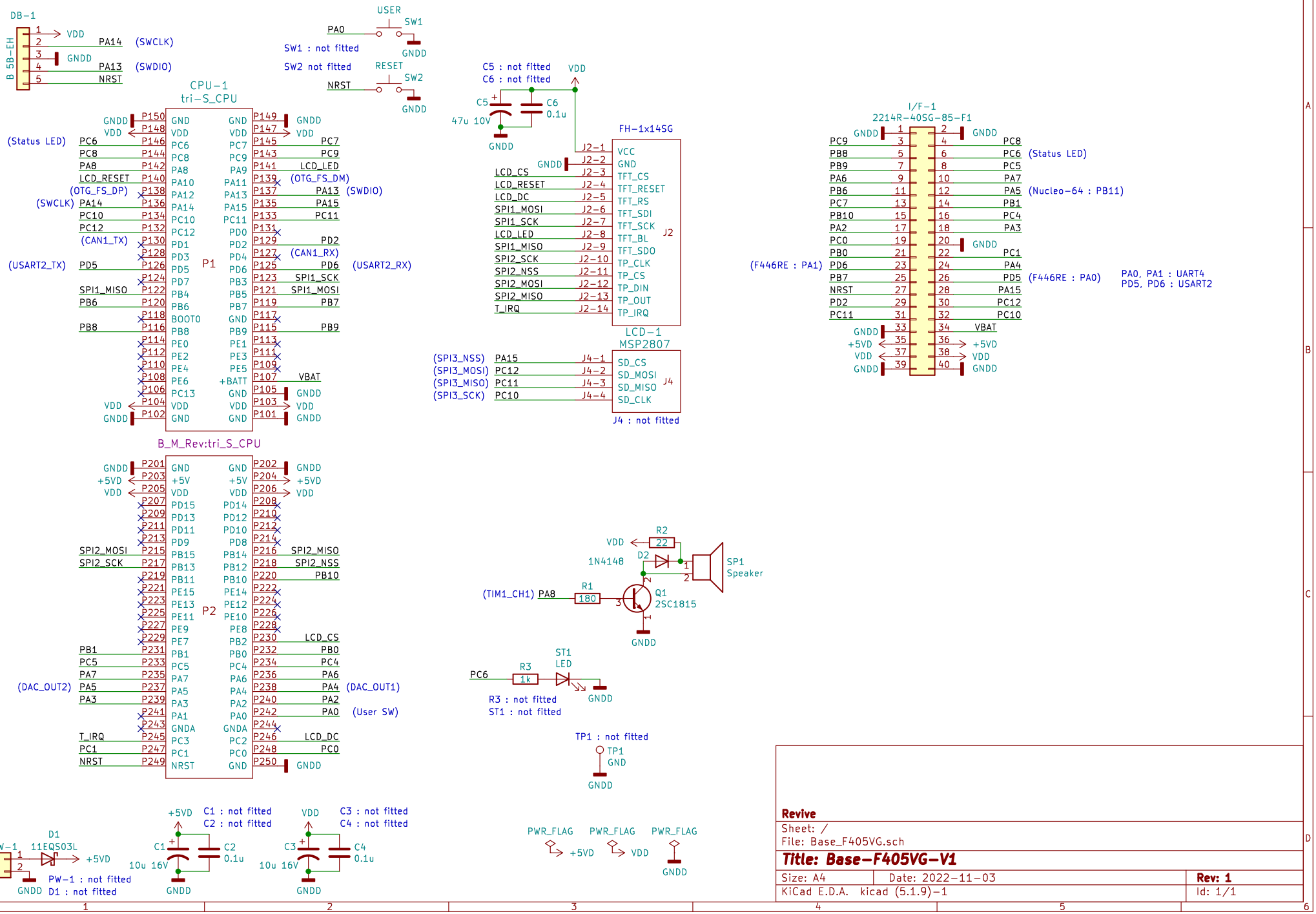
DB-1
B 5B-EH
VDD
PA14 (SWCLK)
GNDD
PA13 (SWDIO)
NRST
USER
SW1
PA0
GNDD
SW1 : not fitted
SW2 not fitted
RESET
SW2
NRST
GNDD
CPU-1
tri-S_CPU
P1
(Status LED) PC6
PC8
PA8
LCD_RESET
(OTG_FS_DP)
(SWCLK) PA14
PC10
PC12
(CAN1_TX)
(USART2_TX) PD5
SPI1_MISO
PB6
PB8
PC7
PC9
LCD_LED
(OTG_FS_DM)
PA13 (SWDIO)
PA15
PC11
PD2
(CAN1_RX)
PD6 (USART2_RX)
SPI1_SCK
SPI1_MOSI
PB7
PB9
VBAT
C5 : not fitted
C6 : not fitted
C5
47u 10V
C6
0.1u
FH-1x14SG
J2
VCC
GND
TFT_CS
TFT_RESET
TFT_RS
TFT_SDI
TFT_SCK
TFT_BL
TFT_SDO
TFT_CLK
TP_CS
TP_DIN
TP_OUT
TP_IRQ
LCD_CS
LCD_RESET
LCD_DC
SPI1_MOSI
SPI1_SCK
LCD_LED
SPI1_MISO
SPI2_SCK
SPI2_NSS
SPI2_MOSI
SPI2_MISO
T_IRQ
LCD-1
MSP2807
J4
SD_CS
SD_MOSI
SD_MISO
SD_CLK
(SPI3_NSS) PA15
(SPI3_MOSI) PC12
(SPI3_MISO) PC11
(SPI3_SCK) PC10
J4 : not fitted
I/F-1
2214R-40SG-85-F1
(F446RE : PA1)
(F446RE : PA0)
(Nucleo-64 : PB11)
PA0, PA1 : UART4
PD5, PD6 : USART2
B_M_Rev:tri_S_CPU
P2
SPI2_MOSI
SPI2_SCK
T_IRQ
PB1
PC5
PA7
(DAC_OUT2) PA5
PA3
PC1
NRST
SPI2_MISO
SPI2_NSS
PB10
LCD_CS
PB0
PC4
PA6
PA4 (DAC_OUT1)
PA2
PA0 (User SW)
LCD_DC
PC0
R2
22
VDD
1N4148
D2
SP1
Speaker
(TIM1_CH1) PA8
R1
180
Q1
2SC1815
GNDD
ST1
LED
PC6
R3
1k
R3 : not fitted
ST1 : not fitted
TP1 : not fitted
TP1
GND
PW-1
B 2B-EH
D1
11EQS03L
+5VD
PW-1 : not fitted
D1 : not fitted
C1 : not fitted
C2 : not fitted
C1
10u 16V
C2
0.1u
C3 : not fitted
C4 : not fitted
C3
C4
PWR_FLAG
Revive
Sheet: /
File: Base_F405VG.sch
Title: Base-F405VG-V1
Size: A4
Date: 2022-11-03
Rev: 1
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Id: 1/1